

Таблица 2. Режимы процессора

| M4-0 | Режим |
|-------------|--------------|
| 1 0 0 0 0 | User |
| 1 0 0 0 1 | FIQ |
| 1 0 0 1 0 | IRQ |
| 1 0 0 1 1 | Supervisor |
| 1 0 1 1 1 | Abort |
| 1 1 0 1 1 | Undefined |
| 1 1 1 1 1 | System |